

FIG. 1

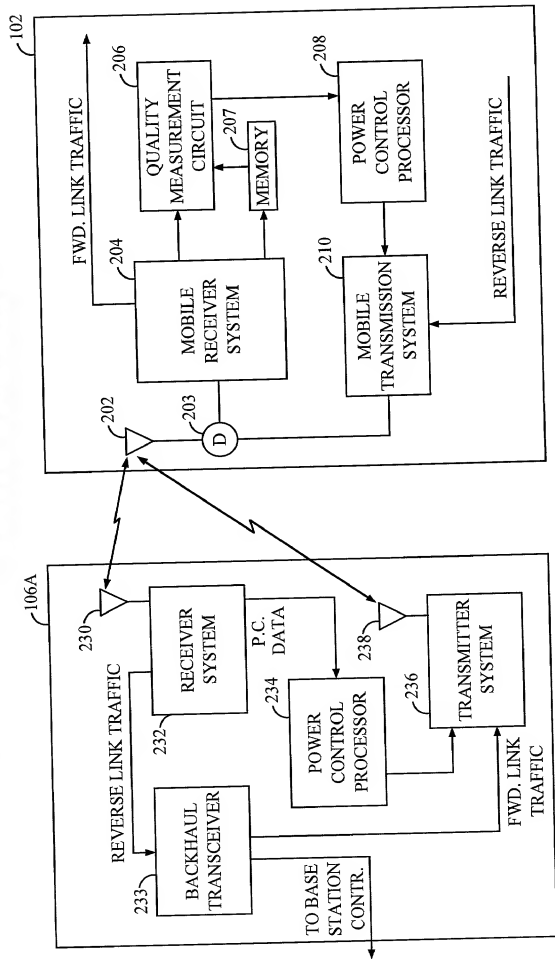
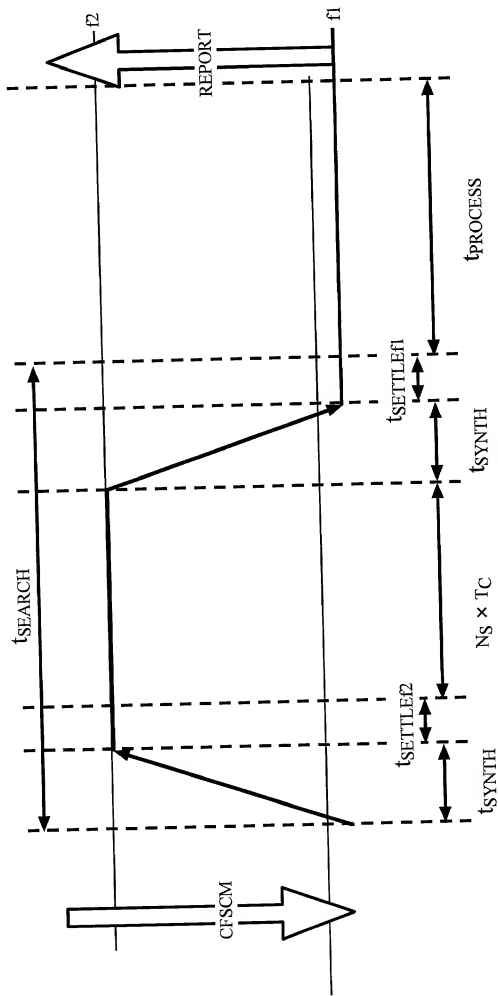


FIG. 2



INTER-FREQUENCY SEARCH EXCURSION TIMING

FIG. 3

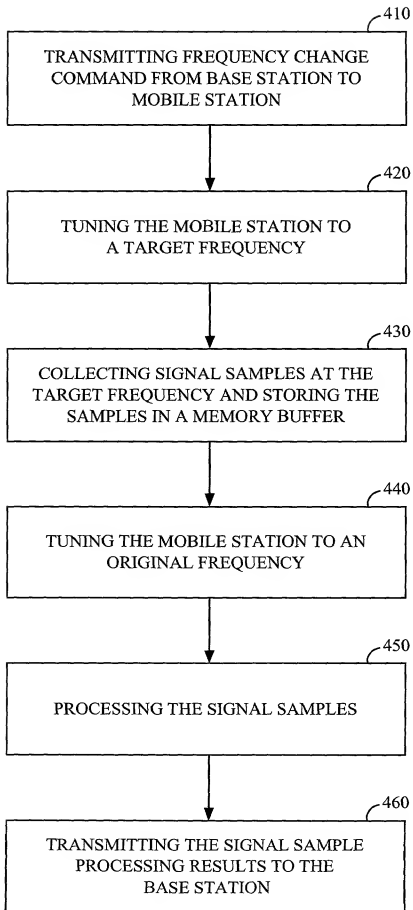
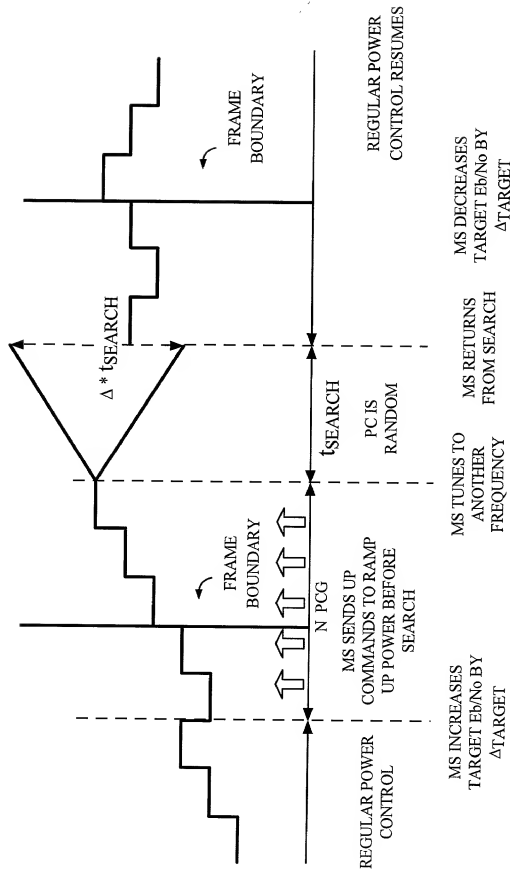
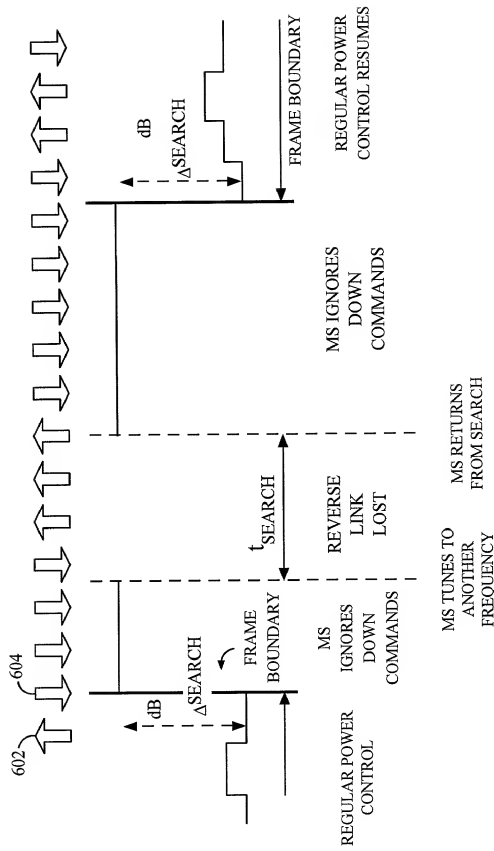


FIG. 4



FORWARD LINK POWER CONTROL INCREASE DURING SEARCH EXCURSION

FIG. 5



REVERSE LINK POWER INCREASE DURING SEARCH VISIT

FIG. 6

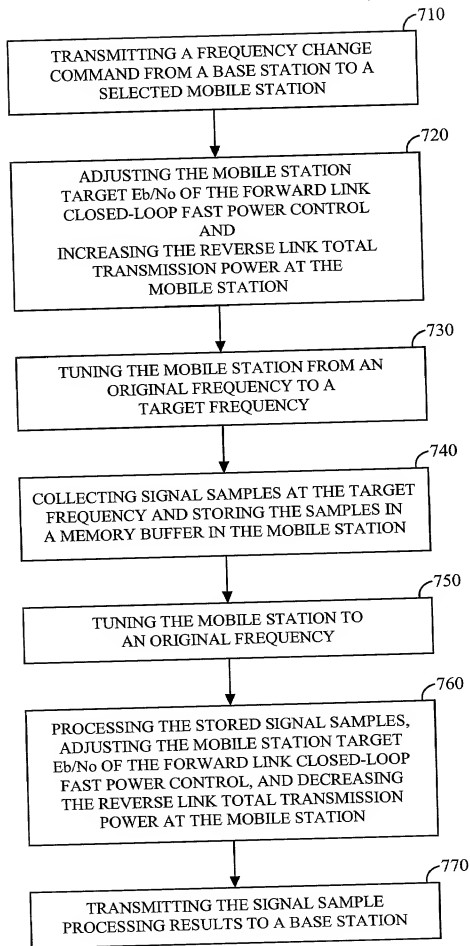


FIG. 7

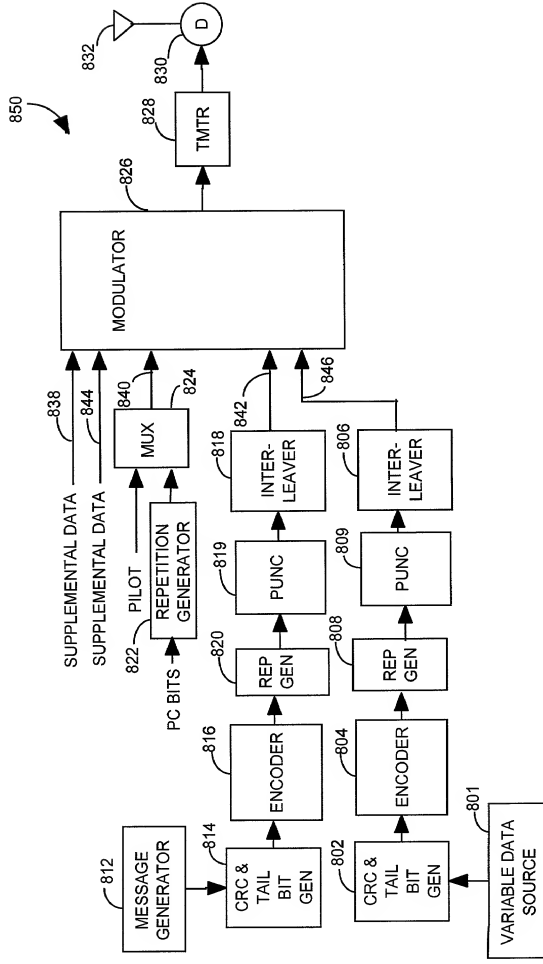


FIG. 8

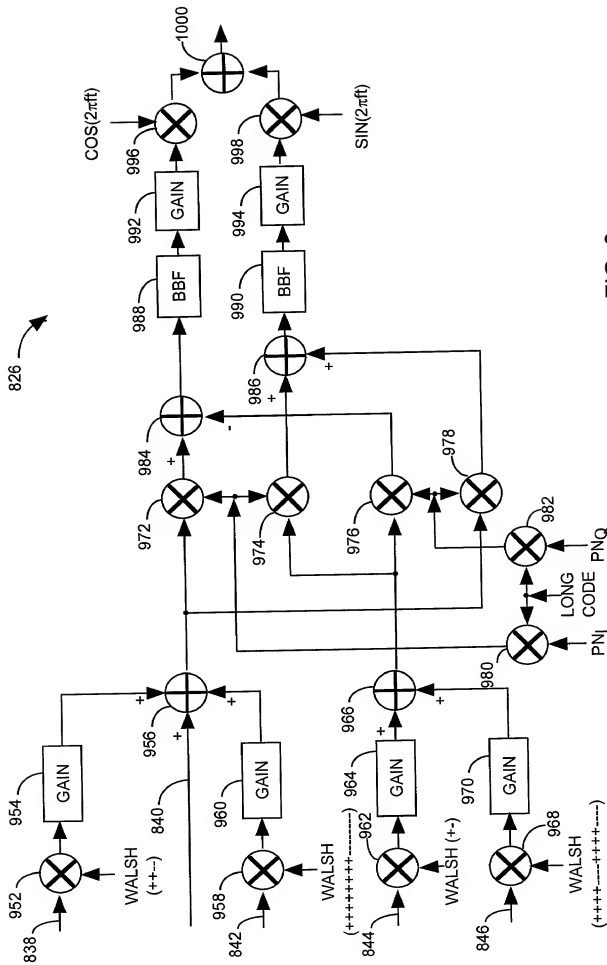
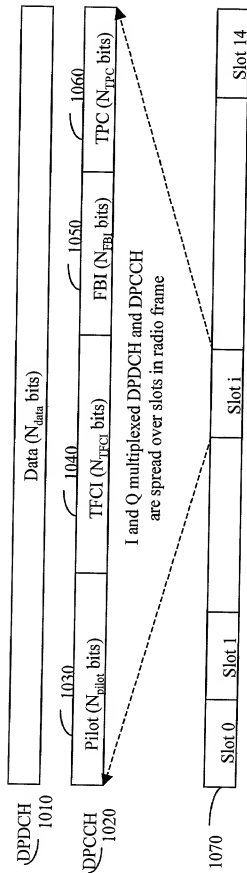


FIG. 9



I radio frame: $T_f = 10\text{ms}$
 $T_{\text{slot}} = 2560 \text{ chips}, 10^6 \cdot \frac{1}{2^k} \text{ bits } (k = 0, \dots, 6)$

FIG. 10

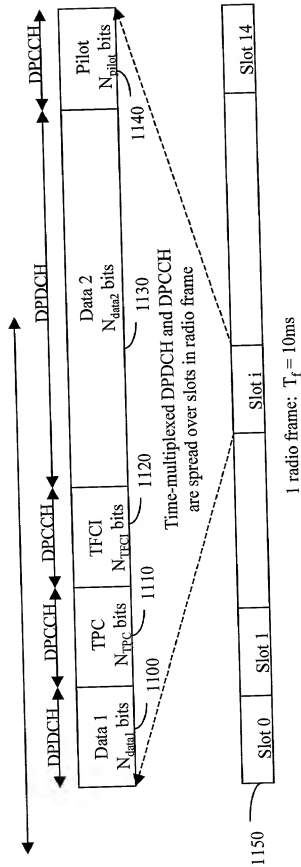


FIG. 11

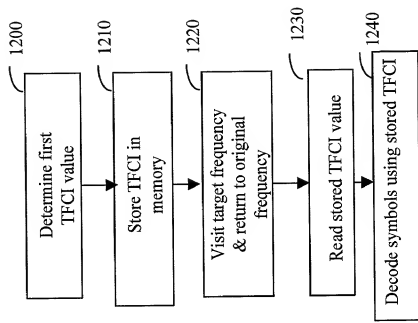


FIG. 12